About the College

Thiagarajar College of Engineering (TCE) is a Government-Aided Autonomous Institution, established in 1957 by the philanthropist Late Karumuttu Thiagarajan Chettiar. TCE is affiliated with Anna University, Chennai, and approved by the All India Council for Technical Education (AICTE). TCE offers a wide array of Undergraduate, Postgraduate and Ph.D Program across various disciplines of Engineering, Architecture and Science. The campus is set within a serene and eco-friendly environment surrounded by dense vegetation and the best infrastructure facilities. TCE has consistently embraced reforms and upgrades in its Teaching-Learning processes. Noteworthy initiatives include the implementation of a Competency-Based Curriculum and Outcome-Based Education. Since 2018, TCE has adopted the Conceive Design Implement Operate (CDIO) framework, shaping curriculum design, pedagogical methods, and assessment techniques with adequate hands-on training for students.

The institution's programs have been accredited by NBA since 1998, indicating compliance with quality standards in technical education. The programmes offered at the institution have garnered numerous accolades, including accreditation by NAAC with a CGPA of 3.56 (out of 4.0) securing A++ Grade in Cycle 2. The institution launched Massive Open Online Courses (MOOCs) in 2021 and introduced the Thiagarajar Research Fellowship (TRF) scheme for PhD research scholars, furthering its commitment to academic excellence and research innovation. In 2024, the institute received a Platinum Certificate in 5S Audit for having a clean and sustainable campus.

About the Department

The FIST Supported Computer Science and Engineering Department was established in the year 1984. The theme area of the department is Knowledge Engineering. It offers 2 UG programme B.E(CSE), B.E CSE (AI&ML) and one PG programme M.E(CSE). The UG programme B.E(CSE) is six years accredited by NBA. The faculty members of the department specialize in the research groups of AI & Data Science, Systems Engineering & High Performance Computing, Software & Data Engineering and Network & Security. Faculty members have publications in Scopus and Web of Science indexed journals from leading publishers like IEEE, Elsevier, Wiley, Taylor & Francis etc. The department has completed many R&D projects from AICTE, UGC, DIT and DST. The department was a consortium partner of CDBR Smart and Secure Environment project sponsored by NTRO, New Delhi. The department is responsible for design and development of College Management Information System which has received many national level recognitions.

About the FDP

Next-generation computing paradigms represent emerging shifts in how we design, operate, and leverage computing systems to handle complex problems and workloads. These paradigms move beyond traditional architectures like classical computing and aim to achieve faster, more efficient, and often more specialized computing solutions. The significance of the course lies in its potential to unleash the computing paradigm practiced in Big Data Applications to solve complex problems.

The objective of this Faculty Development Program (FDP) is to explore the notable trends in Next-generation Computing paradigms. This makes participants to understand computing era. It can provide participants with a solid foundation in next-generation computing paradigms for big data applications, enabling them to effectively address the challenges and opportunities presented by the increasing volume, velocity, and variety of data.
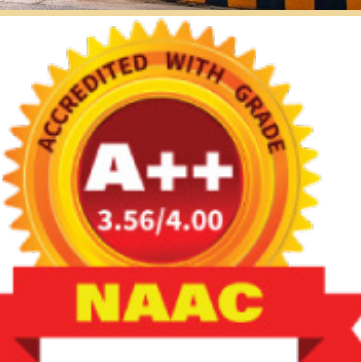
Thiagarajar College of Engineering

67 YEARS
1957-2024
Celebrating Academic Excellence

where quality and ethics matter

One-week Online
Faculty Development Program
on

Next-Gen Computing Paradigms for Bigdata Applications

December 9 - 14, 2024

INSTITUTION'S INNOVATION COUNCIL (Ministry of Education Initiative) | 5S PLATINUM | NBA NATIONAL BOARD OF ACCREDITATION | nirf | NAAC A++ 3.56/4.00

Organized by

Department of Computer Science & Engineering
Thiagarajar College of Engineering
Madurai - 625 015

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TheOfficialTCEPage | tceofficial

Topics to be covered:

- Heterogeneous Computing Hands on: GPU programming
- Embedded Computing Hands on : RISC V
- Cloud Native Computing Hands on : MiniKube
- Edge Computing Hands on : Cloud API/ Open Source Simulators
- Artificial Intelligence Accelerator Hands on : SoC packages and development
- 5G and Beyond Networking Hands on : UAV,V2X

  Session Timing (All Days):

  Session 1: 9.30 am to 11.00 am

  Session 2: 11.15 am to 12.45 pm

  Last date for Registration: 02.12.2024

Registration and Payment Procedure

Online Registration Procedure
Step 1: Make your Registration at *https://forms.gle/KBx25rLNy7r7qMNH6*
Step 2: Complete your Payment
i. Click the link given (or) Search https://eazypay.icicibank.com/
ii. Type "T C E" with Space (as mentioned) in the "Enter Institute Name".
Select the Last Option* ("T C E SOUVENIOR CCE") from the Drop-down menu.
iii. Enter the Required Information (Email id, Mobile number, Name, Designation, College Name, Department, FDP Registration fee) and Title Of The Programme as "NGCP".
Step 3: Pay the fees with GST.
Note down the eazypay Transaction ID and save copy of the receipt
Step 4: Update Google Form with Transaction ID and Receipt copy.
Step 5: Submit the Google Form.

Organizing Committee

Chief Patron
Mr. K. Hari Thiagarajan
Chairman and Correspondent, TCE, Madurai

Patron
Dr. L. Ashok Kumar
Principal, TCE, Madurai

Convener
Dr. S. Mercy Shalinie
Professor, Head and Dean (MIS), Dept. of CSE, TCE, Madurai

Coordinators
Dr. R. Leena Sri
Associate Professor, Dept. of CSE, TCE, Madurai

Ms.C.Santhiya
Assistant Professor, Dept of CSE, TCE, Madurai.

Eligibility

Faculty members, Research Scholars from Engineering Colleges, Arts & Science Colleges and Universities are eligible to apply.

Registration Fees

- The registration fee includes presentation materials and an e-certificate of participation.
- Faculty / Industry Member / Research Scholar / PG Scholar: Rs.885/- (Rs.750+18% GST)

Resource Persons

Ashok Chaudhary
Principal Fujitsu Uvance Engineer, Fujitsu Inc, Japan

Mr. Balaji Visvanathan
Deputy Director-Embedded Systems & Qualcomm CoE, MulticoreWare Inc. Ltd

Mr.Suriya Praksh
Technical Lead-Embedded Systems & Qualcomm CoE MulticoreWare Inc. Ltd

K.Loga Rajeshwaran
Senior Mentor -Data Engineering Big Data & Cloud Analytics GUVI Geek Networks Private Ltd

Mr.Sampbath Narayanan
Director, Dataever Consulting

Mr.Sachin Mudholkar
Lead generative AI IBM

Ms.K.Swetha Kalathinathan
Senior Cloud Security Developer Pfizer

Dr.R.Leena Sri
Associate Professor, Department of CSE, TCE

Contact:

Dr.R.Leena Sri 9443406960 rlsit@tce.edu
Ms.C.Santhiya 9442273897 csit@tce.edu